

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/767,998	02/02/2004	Jamal Ramdani	248412US99DIV	5937
22850	7590 01/11/2005		EXAM	INER
· ·	IVAK, MCCLELLAN	BAUMEISTER	, BRADLEY W	
	1940 DUKE STREET ALEXANDRIA, VA 22314		ART UNIT	PAPER NUMBER
	,		2815	

DATE MAILED: 01/11/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		H-A_
	Application No.	Applicant(s)
Office Action 0	10/767,998	RAMDANI ET AL.
Office Action Summary	Examiner	Art Unit
	B. William Baumeister	2815
The MAILING DATE of this communication Period for Reply	appears on the cover sheet with	1 the correspondence address
A SHORTENED STATUTORY PERIOD FOR RE THE MAILING DATE OF THIS COMMUNICATIO - Extensions of time may be available under the provisions of 37 CFF after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a - If NO period for reply is specified above, the maximum statutory per - Failure to reply within the set or extended period for reply will, by standard properties of the meaning patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no event, however, may a rep. reply within the statutory minimum of thirty (riod will apply and will expire SIX (6) MONTH atute, cause the application to become ABAI	oly be timely filed (30) days will be considered timely. HS from the mailing date of this communication. NDONED (35 U.S.C. § 133).
Status		
1) Responsive to communication(s) filed on $\underline{2}$	<u>5 October 2004</u> .	
2a) ☐ This action is FINAL . 2b) ☑ 1	This action is non-final.	
3) Since this application is in condition for allo	•	
closed in accordance with the practice unde	er Ex parte Quayle, 1935 C.D.	11, 453 O.G. 213.
Disposition of Claims		
4)	drawn from consideration.	quirement.
Application Papers		
9) The specification is objected to by the Exam 10) The drawing(s) filed on is/are: a) Applicant may not request that any objection to Replacement drawing sheet(s) including the cor 11) The oath or declaration is objected to by the	accepted or b) objected to by the drawing(s) be held in abeyand rection is required if the drawing(s	ce. See 37 CFR 1.85(a). s) is objected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for fore a) All b) Some * c) None of: 1. Certified copies of the priority docum 2. Certified copies of the priority docum 3. Copies of the certified copies of the papplication from the International But * See the attached detailed Office action for a	nents have been received. Idents have been received in Appriority documents have been received in Rec	plication No eceived in this National Stage
Attachment(s)		
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB Paper No(s)/Mail Date 10/25/04. 	·	ummary (PTO-413) /Mail Date formal Patent Application (PTO-152)

Application/Control Number: 10/767,998 Page 2

Art Unit: 2815

DETAILED ACTION1

Election/Restrictions

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:

- I. Claims 144-148, drawn to a semiconductor device including a monocrystalline compound semiconductor layer overlying a region of a first monocrystalline semiconductor layer, classified in class 257, subclass 183.
- II. Claims 149-156 and 158-162, drawn to the semiconductor structure of invention I in combination with a monocrystalline metal oxide buffer layer interposed between the semiconductor layer and the compound semiconductor layer, classified in class 257, subclass 190.

The inventions are distinct, each from the other because of the following reasons:

2. Inventions II and I are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because the combination does not require an electrical interconnection (see claim 146). The subcombination has separate utility such as in a device wherein the two semiconductor layers are wafer-bonded directly together without an intervening buffer.

¹ Please note the "Allowable Subject Matter" section following the restriction requirement.

Application/Control Number: 10/767,998 Page 3

Art Unit: 2815

3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

- 4. Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).
- 5. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

Allowable Subject Matter

6. If elected, claims 149-156 and 158-162 would be allowable subject to an update search and the filing of a proper terminal disclaimer. The examination of claims 144-148 would require further consideration and search because at least claim 144 is so broad that it provisionally appears to read on well-known subject matter such as (1) a GaN-based LED mounted on a SiC substrate, which in turn, includes a Zener diode integrated therein; or (2) a Si substrate that includes a SiGe(C) region formed thereon or therein.

Art Unit: 2815

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to B. William Baumeister whose telephone number is (571) 272-1722. The examiner can normally be reached on M-F 8-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217/9197/(to/l-free).

BRADLEY BANASSEER
PRIMARY EXAMINER

B. William Baumeister Primary Examiner Art Unit 2815

January 6, 2004